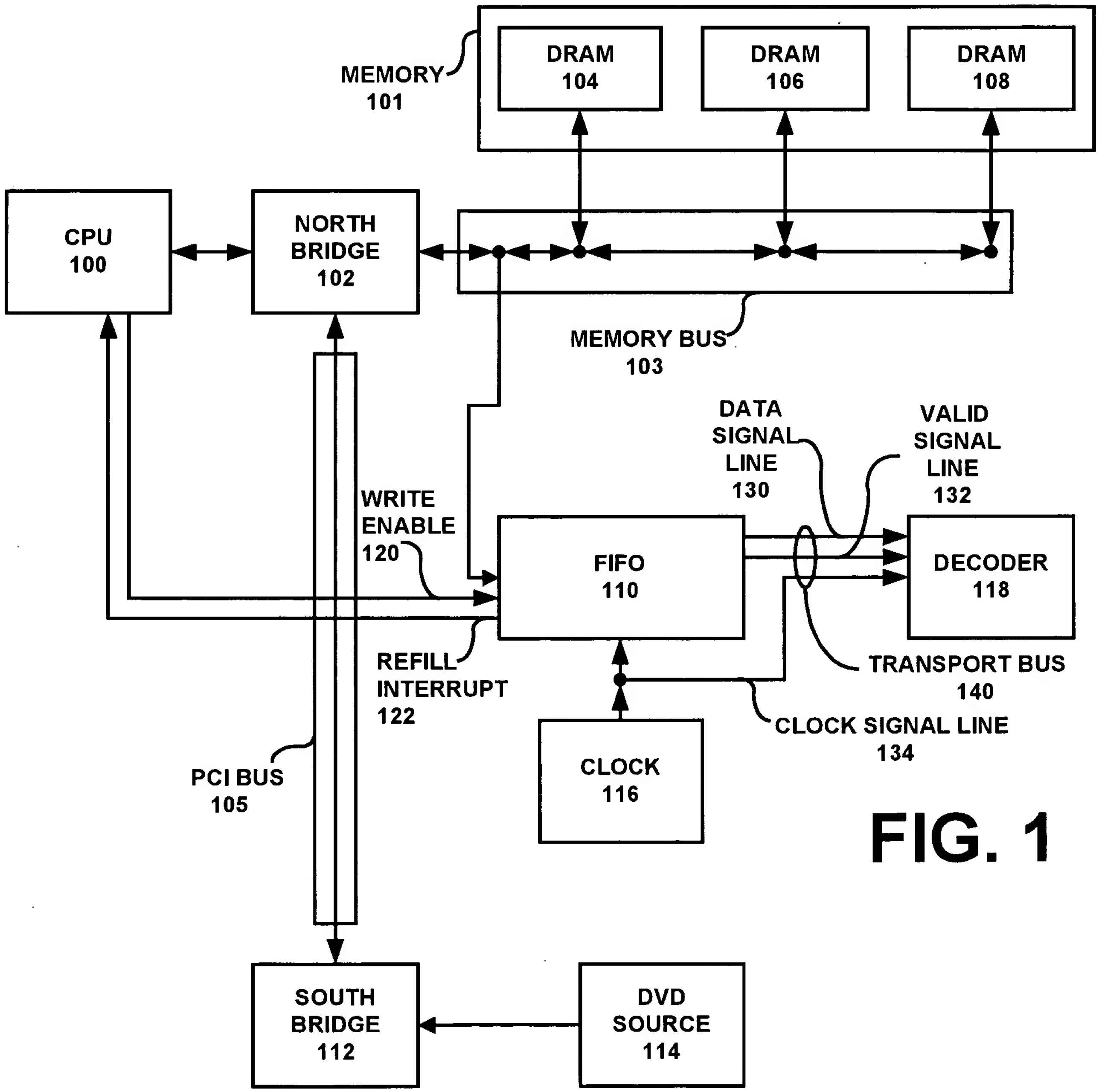
MEMORY 101
DRAM 104
DRAM 106
DRAM 108
CPU 100
NORTH BRIDGE 102
MEMORY BUS 103
DATA SIGNAL LINE 130
VALID SIGNAL LINE 132
WRITE ENABLE 120
FIFO 110
DECODER 118
REFILL INTERRUPT 122
TRANSPORT BUS 140
CLOCK SIGNAL LINE 134
CLOCK 116
PCI BUS 105
SOUTH BRIDGE 112
DVD SOURCE 114

FIG. 1

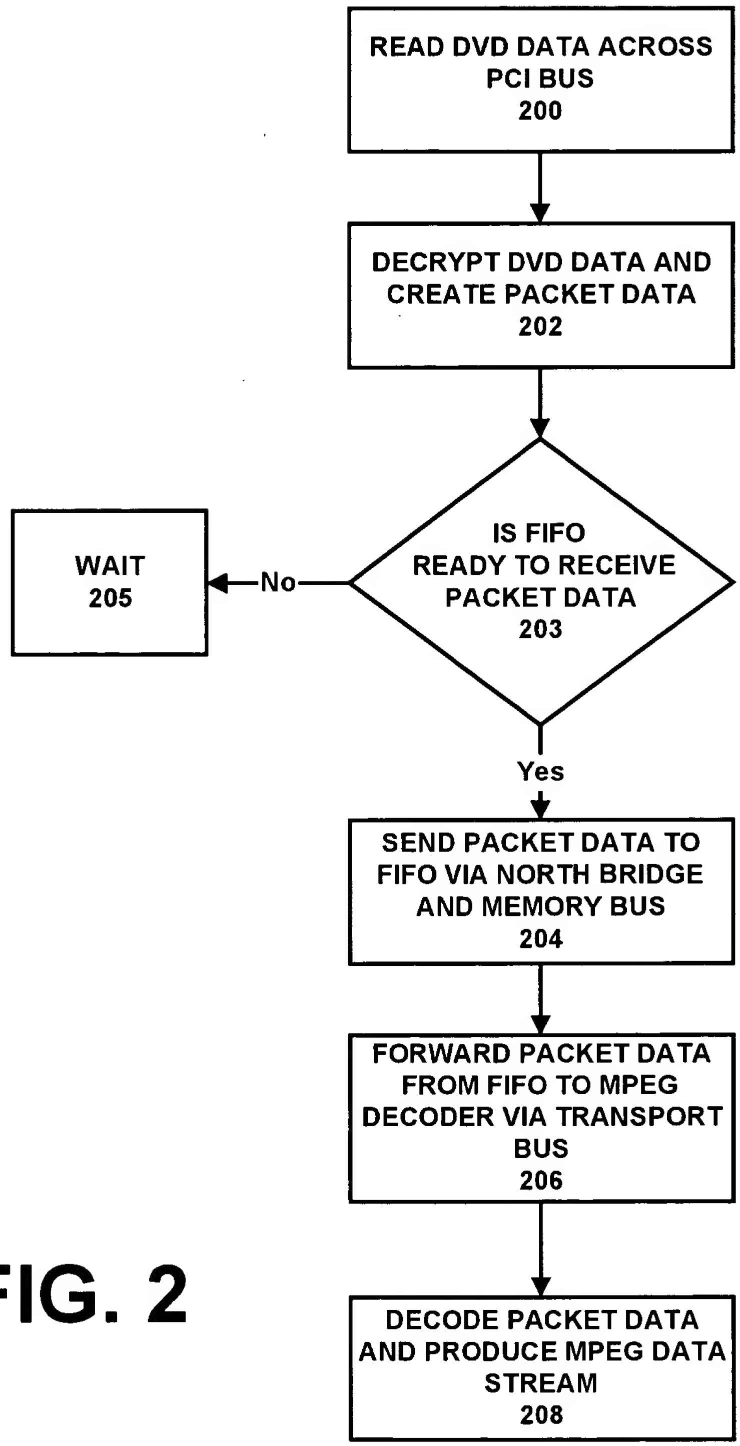

FIG. 2